

A RADIO FREQUENCY RECEIVER ARCHITECTURE WITH  
TRACKING IMAGE-REJECT POLYPHASE FILTERING  
ABSTRACT of the DISCLOSURE

A RF communications receiver is disclosed which permits greater integration on standard silicon chips and consumes less power than previous receivers. A new method for using a tracking polyphase filter for image rejection of variable intermediate frequencies is introduced. This method allows for reduce sensitivity to resistor and capacitor manufacturing variations and allows for the polyphase filter response to be enhanced compared to the prior art.